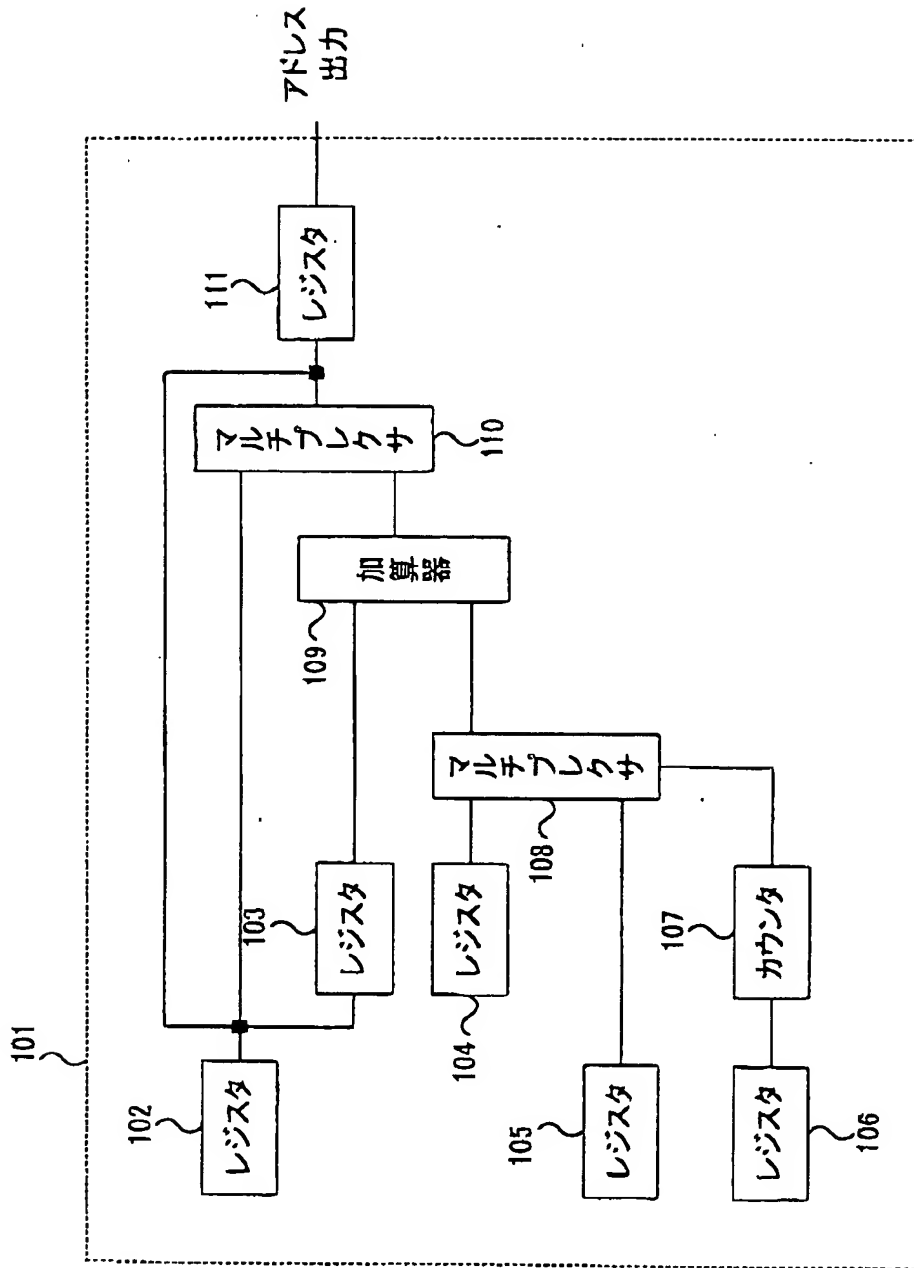


Fig. 1



102, 103, 104, 105, 106, 111: Register

107: Counter

108, 110: Multiplexer

109: Adder

④ Address output

Fig. 2

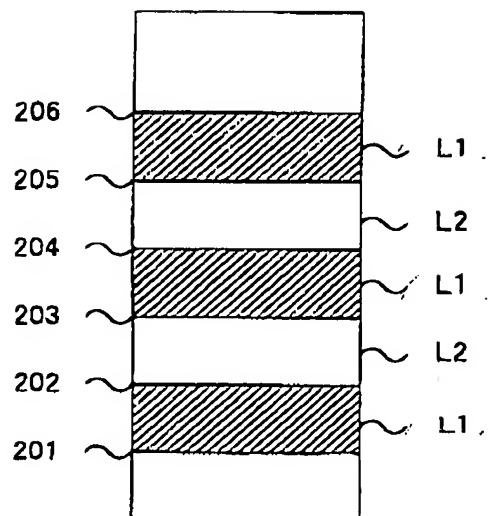


Fig. 3

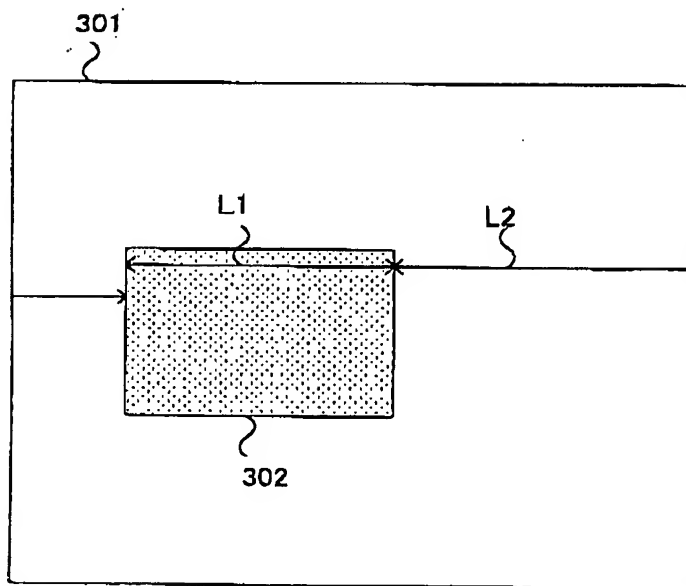


Fig. 4

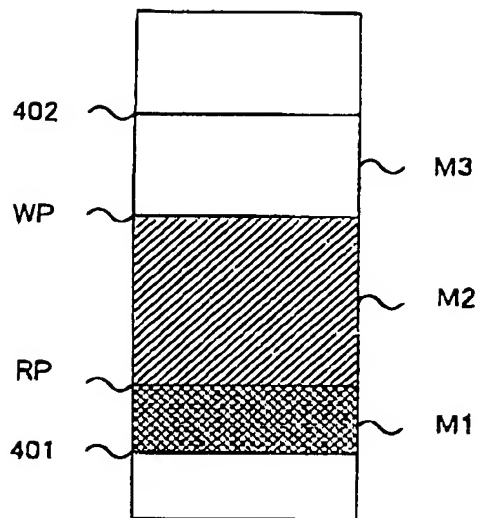


Fig. 5

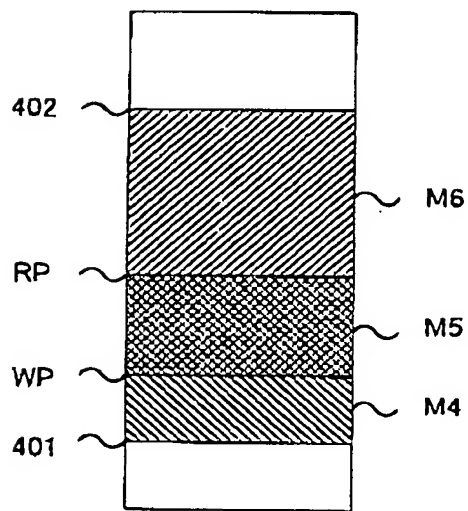
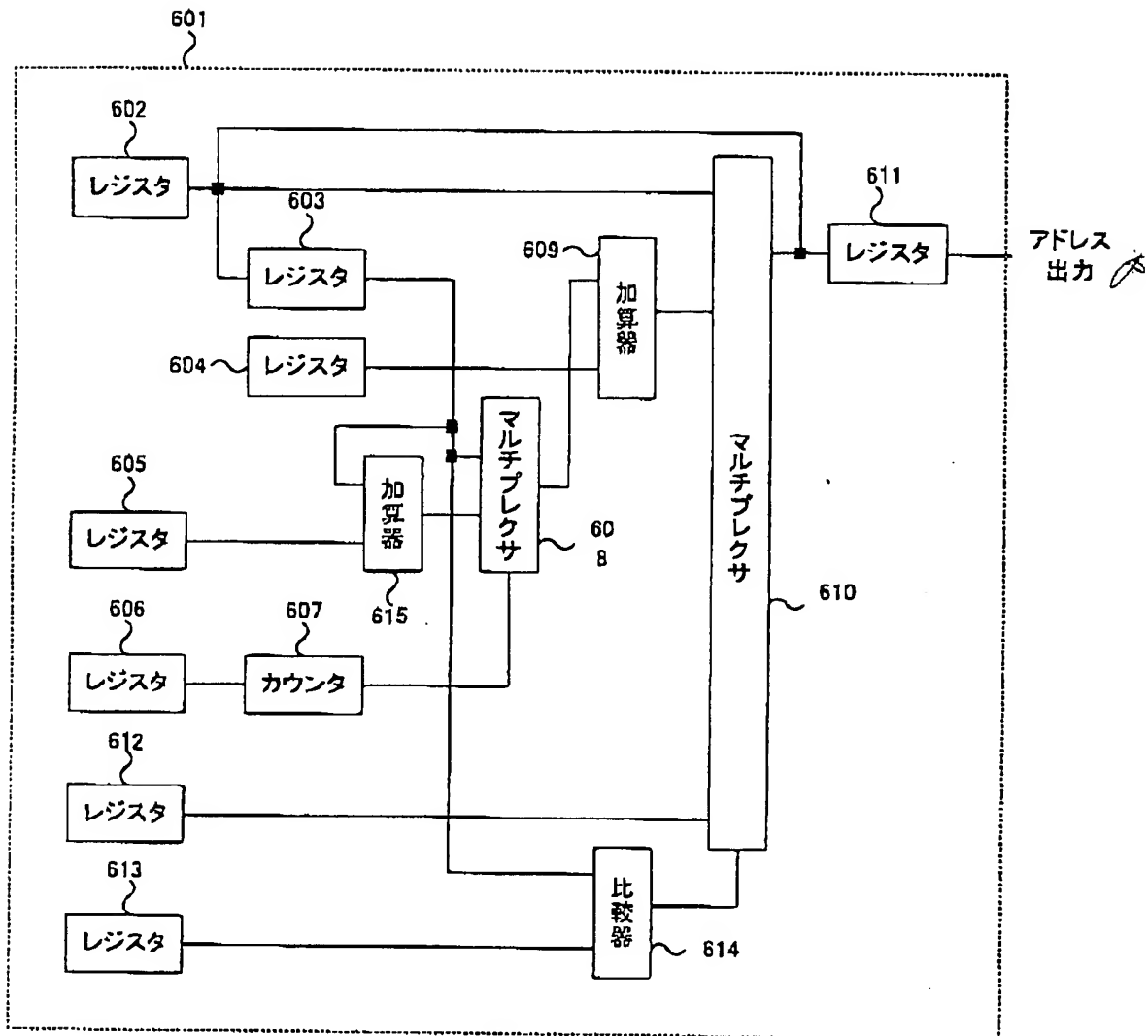


Fig. 6



602, 603, 604, 605, 606, 611, 612, 613: Register

607: Counter

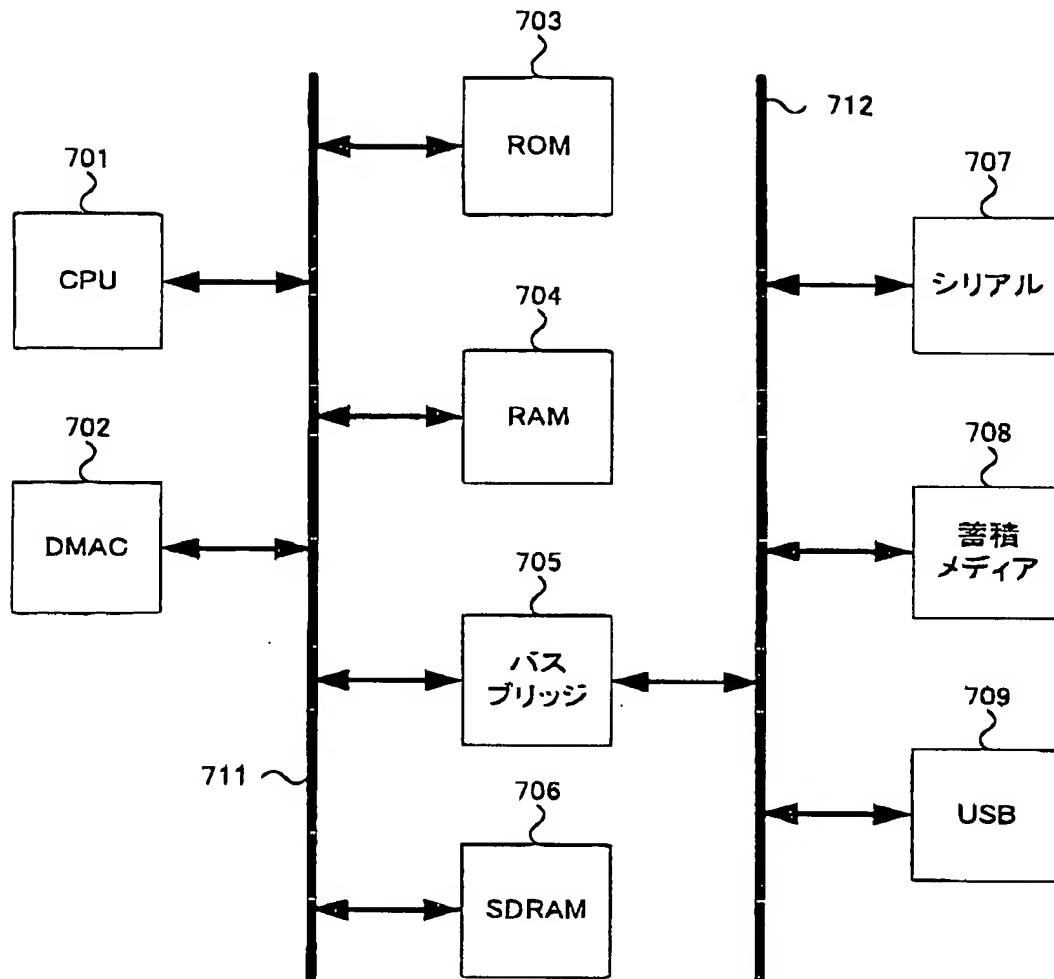
608, 610: Multiplexer

609, 615: Adder

614: Comparator

⌘ Address output

Fig. 7



705: Bus bridge

707: Serial

708: Storage media